

IN THE CLAIMS

Please amend the claims as follows:

1-16. (Cancelled).

17. (New) An electronic circuit comprising conversion means for converting an input voltage into an output voltage, and conversion means comprising:

at least a first energy storage means and a second energy
5 storage means; and

switching means for periodically coupling said at least first and second energy storage means to one another under the control of clock signals so as to store energy in the at least first and second energy storage means, and for transferring at
10 least a portion of the stored energies between the at least first and second energy storage means; and

clock signal generating means for generating the clock signals, said control signal generating means keeping the clock signals in holding states during a holding period during operation,
15 said holding states being equal to the states of the respective clock signals immediately before the holding state.

18. (New) The electronic circuit as claimed in claim 17, characterized in that the switching means and the at least first and second energy storage means are implemented with the use of at least one charge pump.

19. (New) A medium for storage/reading of user information, comprising an integrated circuit comprising the electronic circuit as defined in claim 17.

20. (New) The medium as claimed in claim 19, characterized in that the integrated circuit comprises a photosensitive sensor for providing the input voltage (when the photosensitive sensor receives a substantial quantity of light.

21. (New) The medium as claimed in claim 20, characterized in that the integrated circuit furthermore comprises memory means provided with a supply voltage through utilization of the output voltage.

22. (New) The medium as claimed in claim 21, characterized in that the integrated circuit further comprises a microprocessor and a further photosensitive sensor for providing additional information to the microprocessor, said microprocessor processing
5 the additional information, and said microprocessor being coupled to the memory means for storing the processed additional information.

23. (New) The medium as claimed in claim 21, characterized in that the integrated circuit further comprises a microprocessor and a further photosensitive sensor for providing additional

information to the memory means for storing the additional
5 information, the microprocessor being coupled to the memory means
for processing the additional information after reading of the
additional information from the memory means.

24. (New) The medium as claimed in claim 22, characterized
in that the length of the holding period corresponds by
approximation to that of a time period during which the
photosensitive sensor does not receive a substantial quantity of
5 light.

25. (New) The medium as claimed in claim 24, characterized
in that the microprocessor is idle during the holding period, and
in that the integrated circuit further comprises a standby circuit
for supplying the microprocessor with a supply voltage during the
5 holding period (R_T).

26. (New) The medium as claimed in claim 19, characterized
in that the medium is an optical disc having a side for storing and
reading the user information, wherein the integrated circuit is
fastened to said side of the optical disc in a region not reserved
5 for storing and reading of the user information.

27. (New) The medium as claimed in claim 19, characterized
in that the medium is an optical disc having a first side for

storing and reading of the user information, wherein the integrated circuit is fastened to a second side of the optical disc.

28. (New) A recording/playback device for storage/reading of information onto/from the medium as claimed in claim 19.